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SERIAL NUMBER 09/847,487	FILING DATE 05/02/2001 RULE	CLASS 703	GROUP ART UNIT 2123	ATTORNEY DOCKET NO. SC11499TS
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APPLICANTS

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** CONTINUING DATA

NO. K/MN

** FOREIGN APPLICATIONS

NO. K/MN

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 06/25/2001

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY TX	SHEETS DRAWING 3	TOTAL CLAIMS 27 12	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>K/MN</i>	INITIALS		
Verified and Acknowledged				

ADDRESS

23125

FREESCALE SEMICONDUCTOR, INC.

LAW DEPARTMENT

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78729

TITLE

Method for producing test patterns for testing an integrated circuit

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
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